

## **SPECIFICATION**

### **Please amend the 1<sup>st</sup> paragraph of page 30 as follows:**

--The MAC 170 comprises an integrated circuit or like hardware device providing the functions described herein. It shall be appreciated by those skilled in the art that some MAC services may be implemented in software. The MAC functions implemented herein refer to those MAC functions implemented in hardware that are unique to the present invention. The MAC hardware includes a clock synchronization function 34 (FIG. 2) which is coupled to a plurality of frequency dividers 174, 176, 178 and 180 in which each frequency divider is configured to divide down the clock speed. A plurality of slot allocation units 182, 184, 186 and 188 having different pulse repetition frequencies and different modulation techniques are each coupled to frequency dividers 174, 176, 178 and 180, respectively. Each slot allocation unit 182 through 188 is operatively coupled to a multiplexer/demultiplexer unit 190 which is operatively coupled to an interface to the Physical Layer 192.--

### **Please amend the 2<sup>nd</sup> complete paragraph of page 31 as follows:**

--In operation each slot allocation unit has an associated start time, length, and modulation technique. When the start time occurs, the slot allocation unit will take over control the physical layer through communications with multiplexer/demultiplexer 190. Each slot allocation unit provides data signals having the proper width and proper pulse repetition frequency in the form of data control and clock. At the end of an illustrative slot, as determined by slot length, the slot allocation unit relinquishes control and the following slot

allocation unit has the opportunity to take control during its respective designated transmit time.--

**Please amend the 2<sup>nd</sup> complete paragraph of page 32 as follows:**

--The ~~Mux/Demux~~ multiplexer 190 carries out the operation of merging outgoing bit streams from slot allocation units 182 through 188 into a single signal transmitted to the transmit module 76 and then to pulse generating system 78 and drive system 79.--